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Total Number of Pages in This Submission

Application Number	10/664,112
Filing Date	09/17/2003
First Named Inventor	JONATHAN LYNCH
Art Unit	2817
Examiner Name	CHOE, HENRY
Attorney Docket Number	PD-02W115

ENCLOSURES (Check all that apply)

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S/N 10/664,112

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Jonathan Lynch	Examiner:	Choe, Henry
Serial No.:	10/664,112	Group Art Unit:	2817
Filed:	September 17, 2003	Docket No.:	PD-02W215
Title:	Monolithic Array Amplifier with Periodic Bias-Line Bypassing Structure and Method		
Assignee:	Raytheon Company		

AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111

Commissioner for Patents
P.O. Box 1450
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This responds to the Office Action mailed on February 03, 2005. Please amend the above-identified patent application as follows.